



5. A device as claimed in Claim 2, 3 or 4, in which each converter is in the form of an integrated circuit mounted on a corresponding one of the resistive shunts.

6. A device as claimed in Claim 5, in which each integrated circuit has analog input terminals connected by lead wires to the two copper end portions of the corresponding one of the resistive shunts.

7. A device as claimed in Claim 6, in which the integrated circuit also has a terminal connected to a voltage reference source and includes a second converter for providing a digital signal stream dependent on the voltage on one of the copper end portions of the associated one of the shunts.

